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408-4749082

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: MAJID, Naveed  
Application No.: 09/440595 Conf.: 3147  
Date Filed: 11/15/1999  
Notice of Allowance Date: 4/16/2004

Docket No.: PHA 23843  
Art Unit: 2811  
Examiner: Parekh, Nitin

Title: Multiple Semiconductor Chip (Multi-Chip) Module For Use In High-Power Applications

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Facsimile: (703) 746-4000

**CERTIFICATE OF TRANSMISSION**  
under 37 CFR §1.8

I hereby certify that this correspondence is being facsimile transmitted to the United States and Trademark Office at facsimile number (703) 746-4000, on July 16, 2004. The documents listed below are included in this transmission:

- ✓ Issue Fee Transmittal – ALL required fees paid by deposit account
- ✓ Letter to Official Draftsman
- ✓ One (1) sheets of Formal Drawings

(Signature)

Daniel L. Michalek

(Typed or printed name of person signing Certificate)

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Box ISSUE FEE  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## LETTER TO OFFICIAL DRAFTSMAN

Sir:

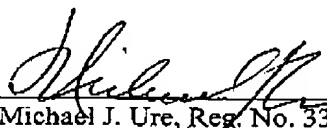
Enclosed please find one (1) sheets of corrected/substitute drawings for entry in the above-referenced patent application.

Please charge any fees which may be required, except the issue fee, or credit any overpayment to Deposit Account No. 14-1270.

Date: 7/16/04

Respectfully submitted,

By

  
Michael J. Ure, Reg. No. 33,089  
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## CERTIFICATE OF MAILING OR TRANSMISSION

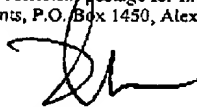
I hereby certify that on the date indicated below this correspondence is being faxed to or deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450".

(Date)

16-JUL-04

(Signature)

(Name)

  
Daniel L. Michalek